



**UNITED STATES DEPARTMENT OF COMMERCE
Patent and Trademark Office**

Address: COMMISSIONER OF PATENTS AND TRADEMARKS
Washington, D.C. 20231

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
09/017,224	02/02/98	HANSEN	C 43876-089

MCDERMOTT WILL AND EMERY
600 THIRTEENTH STREET NW
SUITE 1200
WASHINGTON DC 20005-3096

LMC1/1213

EXAMINER

WEBSTER, B

ART UNIT	PAPER NUMBER
----------	--------------

2731

DATE MAILED: 12/13/99

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

Office Action Summary

Application No.
09/017,224

Applicant(s)
Hansen et al.

Examiner
Bryan Webster

Group Art Unit
2731



☒ Responsive to communication(s) filed on Feb 2, 1998

☐ This action is **FINAL**.

☐ Since this application is in condition for allowance except for formal matters, **prosecution as to the merits is closed** in accordance with the practice under *Ex parte Quayle*, 35 C.D. 11; 453 O.G. 213.

A shortened statutory period for response to this action is set to expire 3 month(s), or thirty days, whichever is longer, from the mailing date of this communication. Failure to respond within the period for response will cause the application to become abandoned. (35 U.S.C. § 133). Extensions of time may be obtained under the provisions of 37 CFR 1.136(a).

Disposition of Claim

☒ Claim(s) 1-8 is/are pending in the application

Of the above, claim(s) _____ is/are withdrawn from consideration

☐ Claim(s) _____ is/are allowed.

☒ Claim(s) 1 and 5 is/are rejected.

☒ Claim(s) 2-4 and 6-8 is/are objected to.

☐ Claims _____ are subject to restriction or election requirement.

Application Papers

☒ See the attached Notice of Draftsperson's Patent Drawing Review, PTO-948.

☐ The drawing(s) filed on _____ is/are objected to by the Examiner.

☐ The proposed drawing correction, filed on _____ is ☐ approved ☐ disapproved.

☐ The specification is objected to by the Examiner.

☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

☐ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).

☐ All ☐ Some* ☒ None of the CERTIFIED copies of the priority documents have been

☐ received.

☐ received in Application No. (Series Code/Serial Number) _____

☐ received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

*Certified copies not received: _____

☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

Attachment(s)

☒ Notice of References Cited, PTO-892

☐ Information Disclosure Statement(s), PTO-1449, Paper No(s). _____

☐ Interview Summary, PTO-413

☒ Notice of Draftsperson's Patent Drawing Review, PTO-948

☐ Notice of Informal Patent Application, PTO-152

--- SEE OFFICE ACTION ON THE FOLLOWING PAGES ---

Art Unit: 2731

DETAILED ACTION

Drawings

1. This application has been filed with informal drawings which are acceptable for examination purposes only. Formal drawings will be required when the application is allowed.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1 and 5 are rejected under 35 U.S.C. 102(b) as being anticipated by **Snyder** (Newly cited US Patent No. 5,422,914).

Consider claims 1 and 5, Snyder recites a system for synchronizing data between two devices operating at different clock frequencies comprising; receiving the data stream at a rate of the first clock, loading (sequentially) the data stream into a plurality of registers at a first clock rate (14,16); a control circuitry (34) which generates a synchronization signal having a frequency proportional to a ratio of the first clock rate and a second clock (See abstract); reading the plurality of registers at a rate corresponding to the frequency of the synchronization signal. (See abstract, summary of the invention, col.3, line 42 to col. 4, lines 21 and col.5, line 4 to col.6, line 43).

Art Unit: 2731

Allowable Subject Matter

4. Claims 2-4,6-8 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion


5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. References Fukasawa, Nguyen, Mazzurco et al., Lang, Etienne, Schoffel, Satoh, Satou, Elliott et al. are cited as being pertinent to the instant invention. The cited invention illustrated systems which attempt to synchronize at received clock rate operating at a rate different than the rate which the receiver unit operates.

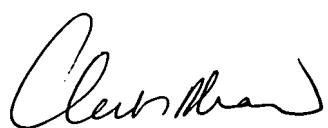
6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Bryan Webster whose telephone number is (703) 308-6607.

Application/Control Number: 09/017,224

Page 4

Art Unit: 2731

Bwebster

December 8, 1999


CHI H. PHAM
SUPERVISORY PATENT EXAMINER
GROUP 2700 12/10/99